Data Representation in Memory

CSCI 2400 / ECE 3217: Computer Architecture

Instructor:

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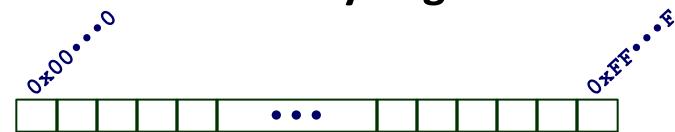
Slides adapted from Bryant & O'Hallaron's slides via Jason Fritts

Data Representation in Memory

Basic memory organization

- Bits & Bytes basic units of Storage in computers
- Representing information in binary and hexadecimal
- Representing Integers
 - Unsigned integers
 - Signed integers
- Representing Text
- Representing Pointers

Byte-Oriented Memory Organization



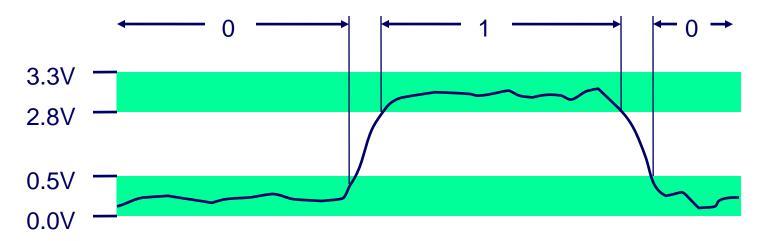
- Modern processors: Byte-Addressable Memory
 - Conceptually a very large array of bytes
 - Each byte has a unique address
 - Processor address space determines address range:
 - 32-bit address space has 2³² unique addresses: 4GB max
 - 0x00000000 to 0xffffffff (in decimal: 0 to 4,294,967,295)
 - 64-bit address space has 2⁶⁴ unique addresses: ~ 1.8x10¹⁹ bytes max

 - Enough to give everyone on Earth about 2 Gb
 - Address space size is not the same as processor size!
 - E.g.: The original Nintendo was an 8-bit processor with a 16-bit address space

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Why Use Bits & Binary?



- Digital transistors operate in high and low voltage ranges
- Voltage Range dictates Binary Value on wire
 - high voltage range (e.g. 2.8V to 3.3V) is a logic 1
 - Iow voltage range (e.g. 0.0V to 0.5V) is a logic 0
 - voltages in between are indefinite values
- Ternary or quaternary systems have practicality problems

Bits & Bytes

Computers use bits:

- a "bit" is a base-2 digit
- {L, H} => {0, 1}

Single bit offers limited range, so grouped in bytes

- 1 byte = 8 bits
- a single datum may use multiple bytes

Data representation 101:

- Given N bits, can represent 2^N unique values
 - Letters of the alphabet?
 - Colors?

Encoding Byte Values

Processors generally use multiples of Bytes

- common sizes: 1, 2, 4, 8, or 16 bytes
- Intel data names:

 Byte 	1 byte	(8 bits)	2 ⁸ = 256	
 Word 	2 bytes	(16 bits)	2 ¹⁶ = 65,536	
 Double word 	4 bytes	(32 bits)	2 ³² = 4,294,967,295	
 Quad word 	8 bytes	(64 bits)		
	2 ⁶⁴	2 ⁶⁴ = 18,446,744,073,709,551,616		

Unfortunately, these names are not standard so we'll often use C data names instead (but these vary in size too... /sigh)

C Data Types

32-bit

64-bit

C Data Type	Typical 32-bit	Intel IA32	x86-64	
char	1 byte	1	1	
short	2	2	2	
int	4	4	4	
long	4	4	8	R
long long	8	8	8	
float	4	4	4	key
double	8	8	8	differences
long double	8	10/12	10/16	
pointer (addr)	4	4	8	¥

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Encoding Byte Values

- 1 Byte = 8 bits
 - Binary: 000000002 to 11111112
- A byte value can be interpreted in many ways!
 - depends upon how it's used

For example, consider byte with: 01010101₂

- as ASCII text:
- as integer:
- as IA32 instruction:
- the 86th byte of memory in a computer
- a medium gray pixel in a gray-scale image
- could be interpreted MANY other ways...

'U' 85₁₀ pushl %ebp

Binary is Hard to Represent!

Problem with binary – Cumbersome to use

- e.g. approx. how big is: 10100111010001011101011₂?
- Would be nice if the representation was closer to decimal: 21,930,731

Let's define a larger base so that

$$R^1=2^x$$

- for equivalence, R and x must be integers then 1 digit in R equals x bits
- equivalence allows direct conversion between representations
- two options closest to decimal:
 - octal: $8^1 = 2^3$ (base eight)
 - hexadecimal: $16^1 = 2^4$ (base sixteen)

Representing Binary Efficiently

Octal or Hexadecimal?

- binary : 10100111010001011101011₂
 octal: 123521353₈
 hexadecimal number: 14EA2EB₁₆
 decimal: 21930731
- Octal and Hex are closer in size to decimal, BUT...
- How many base-R digits per byte?
 - Octal: 8/3 = 2.67 octal digits per byte -- BAD
 - Hex: 8/4 = 2 hex digits per byte -- GOOD

Hexadecimal wins: 1 *hex digit* \Leftrightarrow 4 *bits*

Expressing Byte Values

Juliet: "What's in a name? That which we call a rose By any other name would smell as sweet."

Common ways of expressing a byte

- Binary: 000000002 to 11111112
- Decimal: 0₁₀ to 255₁₀
- Hexadecimal: 00₁₆ to FF₁₆
 - Base-16 number representation
 - Use characters '0' to '9' and 'A' to 'F'
 - in C/C++ programming languages, D3₁₆ written as either
 - 0xD3
 - 0xd3

Decimal vs Binary vs Hexadecimal

Decimal	Binary	Hexadecimal
0	0000	0
1	0001	1
2	0010	2
3	0011	3
4	0100	4
5	0101	5
6	0110	6
7	0111	7
8	1000	8
9	1001	9
10	1010	А
11	1011	В
12	1100	С
13	1101	D
14	1110	E
15	1111	F
16	10000	10
17	10001	11
18	10010	12

Convert Between Binary and Hex

Convert Hexadecimal to Binary

- Simply replace each hex digit with its equivalent 4-bit binary sequence
- Example:

0110 1101 0001 1001 1111 0011 1100₂

6 D 1 9 F 3 C₁₆

Convert Binary to Hexadecimal

- <u>Starting from the radix point</u>, replace each sequence of 4 bits with the equivalent hexadecimal digit
- Example: 1011001000110101110101100010100112

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Unsigned Integers – Binary

Computers store Unsigned Integer numbers in Binary (base-2)

- Binary numbers use place valuation notation, just like decimal
- Decimal value of *n*-bit unsigned binary number:

$$value_{10} = \sum_{i=0}^{n-1} a_i * 2^i$$

$$0 \quad 1 \quad 1 \quad 1 \quad 0 \quad 1 \quad 0 \quad 1$$

$$2^7 \quad 2^6 \quad 2^5 \quad 2^4 \quad 2^3 \quad 2^2 \quad 2^1 \quad 2^0$$

$$value_{10} = 0 * 2^7 + 1 * 2^6 + 1 * 2^5 + 1 * 2^4 + 0 * 2^3 + 1 * 2^2 + 0 * 2^1 + 1 * 2^0$$

$$= 2^6 + 2^5 + 2^4 + 2^2 + 2^0$$

$$= 64 + 32 + 16 + 4 + 1 = 117_{10}$$

Unsigned Integers – Base-R

Convert Base-R to Decimal

- Place value notation can similarly determine decimal value of any base, R
- Decimal value of *n*-digit base *r* number:

$$value_{10} = \sum_{i=0}^{n-1} a_i * r^i$$

• Example: $317_8 = ?_{10}$

$$value_{10} = 3 * 8^{2} + 1 * 8^{1} + 7 * 8^{0}$$
$$= 3 * 64 + 1 * 8 + 7 * 1$$
$$= 192 + 8 + 7 = 207_{10}$$

Unsigned Integers – Hexadecimal

Commonly used for converting hexadecimal numbers

- Hexadecimal number is an "equivalent" representation to binary, so often need to determine decimal value of a hex number
- Decimal value for *n*-digit hexadecimal (base 16) number:

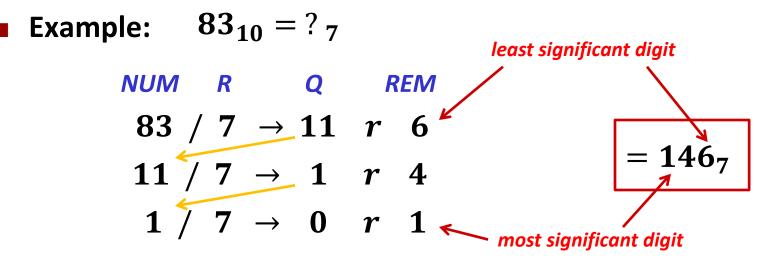
$$value_{10} = \sum_{i=0}^{n-1} a_i * 16^i$$

• Example: $9E4_{16} = ?_{10}$

$$value_{10} = 9 * 16^{2} + 14 * 16^{1} + 4 * 16^{0}$$
$$= 9 * 256 + 14 * 16 + 4 * 1$$
$$= 2304 + 224 + 4 = 2532_{10}$$

Unsigned Integers – Convert Decimal to Base-R

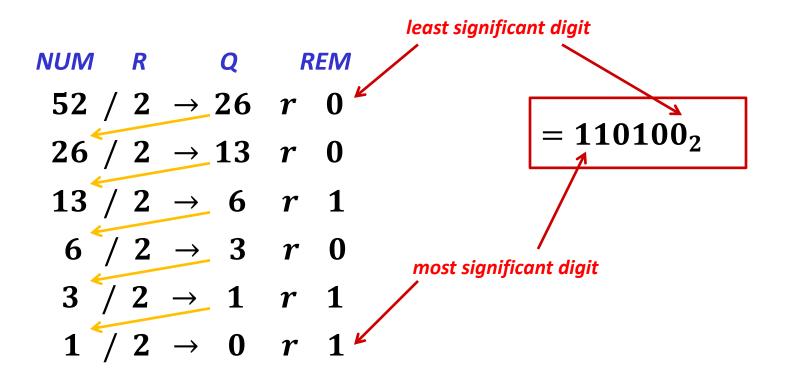
- Also need to convert decimal numbers to desired base
- Algorithm for converting unsigned Decimal to Base-R
 - a) Assign decimal number to *NUM*
 - b) Divide *NUM* by *R*
 - Save remainder *REM* as next least significant digit
 - Assign quotient *Q* as new *NUM*
 - c) Repeat step b) until quotient Q is zero



Unsigned Integers – Convert Decimal to Binary

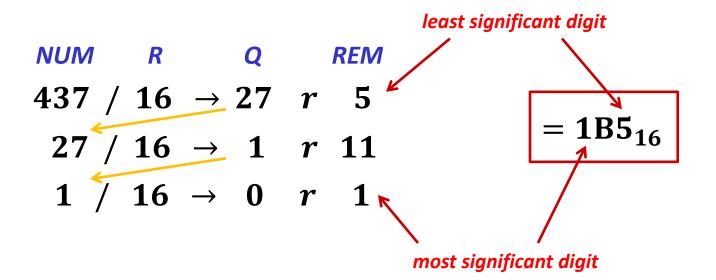
Example with Unsigned Binary:

 $52_{10} = ?_2$



Unsigned Integers – Convert Decimal to Hexadecimal

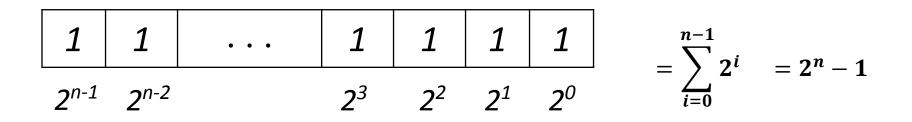
Example with Unsigned Hexadecimal: 437₁₀ = ? 16



Unsigned Integers – Ranges

Range of Unsigned binary numbers based on number of bits

- Given representation with n bits, min value is always sequence
 - 0....0000 = 0
- Given representation with n bits, max value is always sequence
 - 1....1111 = $2^n 1$
- So, ranges are:
 - unsigned char: $0 \rightarrow 255$ $(2^8 1)$
 - unsigned short: $0 \rightarrow 65, 535$ $\left(2^{16}-1\right)$
 - unsigned int: $0 \rightarrow 4, 294, 967, 295$ $(2^{32} 1)$



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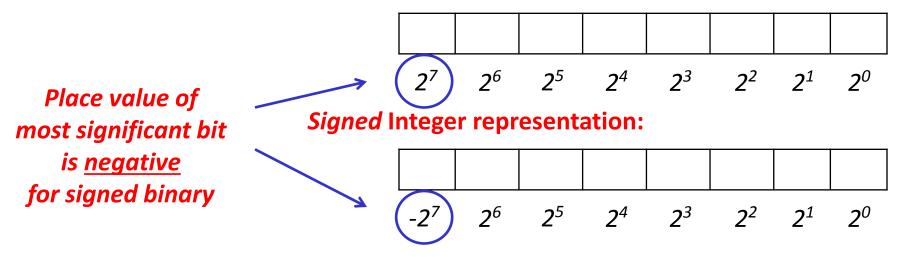
Representing Integers

- Unsigned integers
- Signed integers
- Representing Text
- Representing Pointers

Signed Integers – Binary

- Signed Binary Integers converts half of range as negative
- Signed representation identical, except for most significant bit
 - For signed binary, most significant bit indicates sign
 - 0 for nonnegative
 - 1 for negative
 - Must know number of bits for signed representation

Unsigned Integer representation:



Signed Integers – Binary

Decimal value of *n*-bit signed binary number:

$$value_{10} = -a_{n-1} * 2^{n-1} + \sum_{i=0}^{n-2} a_i * 2^i$$

Positive (in-range) numbers have same representation:

Unsigned Integer representation:

Signed Integer representation:

Signed Integers – Binary

- Only when most significant bit set does value change
- Difference between unsigned and signed integer values is 2^N

Unsigned Integer representation:

$$= 105 + 128_{10}$$
$$= 233_{10}$$

Signed Integer representation:

$$= 105 - 128_{10}$$
$$= -23_{10}$$

Quick Check:

For an 8-bit representation:

- What bit pattern has the minimum value?
- What bit pattern has the maximum value?
- What bit pattern represents 0?
- What bit pattern represents -1?

Signed Integers – Ranges

Range of Signed binary numbers:

- Given representation with *n* bits, min value is always sequence
 - $100....0000 = -2^{n-1}$
- Given representation with *n* bits, max value is always sequence
 - $011....1111 = 2^{n-1} 1$
- So, ranges are:

C data type	# bits	Unsigned range	Signed range
char	8	$0 \rightarrow 255$	-128 → 127
short	16	$0 \rightarrow 65,535$	$-32,768 \rightarrow 32,767$
int	32	$0 \rightarrow 4,\!294,\!967,\!295$	$-2,147,483,648 \rightarrow 2,147,483,647$

Signed Integers – Convert to/from Decimal

Convert Signed Binary Integer to Decimal

- Easy just use place value notation
 - two examples given on last two slides

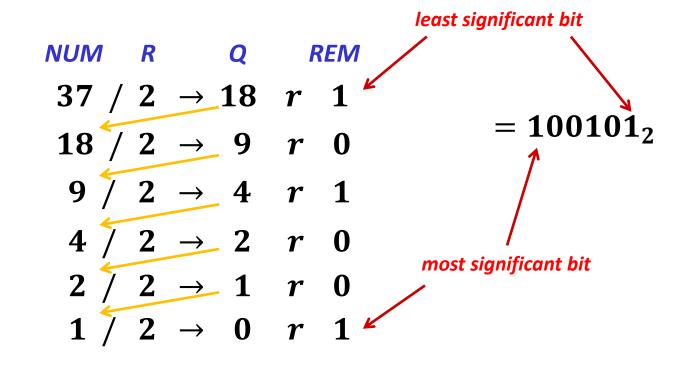
Convert Decimal to Signed Binary Integer

- MUST know <u>number of bits</u> in signed representation
- Algorithm:
 - a) Convert magnitude (abs val) of decimal number to unsigned binary
 - b) Decimal number originally negative?
 - If positive, conversion is <u>done</u>
 - If negative, perform negation on answer from part a)
 - » zero extend answer from a) to N bits (size of signed repr)
 - » negate: flip bits and add 1

Signed Integers – Convert Decimal to Base-R

Example: $-37_{10} = ?_{8-bit signed}$

• A)
$$|-37_{10}| = ?_2$$



Signed Integers – Convert Decimal to Base-R

Example: $-37_{10} = ?_{8-bit signed}$

- B) -37₁₀ was negative, so perform *negation*
 - zero extend 100101 to 8 bits

 $100101_2 \rightarrow 00100101_2$

- negation
 - flip bits: 00100101_2 \downarrow 11011010_2 - add 1: + 1_2 11011011_2



Can validate answer using place value notation

Quick check:

For an 8-bit representation:

Convert 67₁₀ into a signed integer

Signed Integers – Convert Decimal to Base-R

Example: $67_{10} = ?_{8-bit \, signed}$

• A)
$$|67_{10}| = ?_2$$

NUM R Q REM least significant bit
 $67 / 2 \rightarrow 33 \ r \ 1$
 $33 / 2 \rightarrow 16 \ r \ 1$
 $16 / 2 \rightarrow 8 \ r \ 0$
 $8 / 2 \rightarrow 4 \ r \ 0$
 $4 / 2 \rightarrow 2 \ r \ 0$ most significant bit
 $2 / 2 \rightarrow 1 \ r \ 0$
 $1 / 2 \rightarrow 0 \ r \ 1$

Signed Integers – Convert Decimal to Base-R

Example:
$$67_{10} = ?_{8-bit signed}$$

B) 67₁₀ was positive, so <u>done</u>

$= 1000011_{2}$

Can validate answer using place value notation

Quick check:

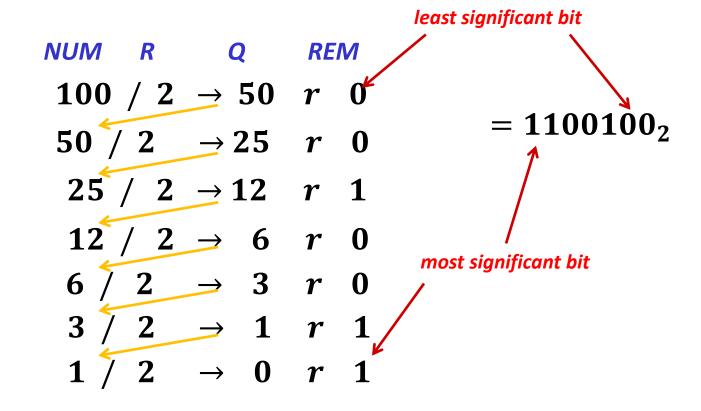
For an 8-bit representation:

Convert -100₁₀ into a signed integer

Signed Integers – Convert Decimal to Base-R

Example: $-100_{10} = ?_{8-bit signed}$

• A)
$$|-100_{10}| = ?_2$$



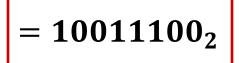
Signed Integers – Convert Decimal to Base-R

Example: $-100_{10} = ?_{8-bit signed}$

- B) -100₁₀ was negative, so perform *negation*
 - zero extend 100101 to 8 bits

 $1100100_2 \ \rightarrow \ 01100100$

- negation
 - flip bits: 01100100_2 \downarrow 10011011_2 - add 1: + 1_2 10011100_2



Can validate answer using place value notation

Signed Integers – Convert Decimal to Base-R

- Be careful of range!
- **Example:** $-183_{10} = ?_{8-bit signed}$

• A)
$$|-183_{10}| = ?_2 = 10110111_2$$

- B) -183₁₀ was negative, so perform *negation*
 - zero extend 10110111 to 8 bits // already done
 - negation

- flip bits:
$$10110111_2$$

 \downarrow
 01001000_2
- add 1: $+ 1_2$
 $01001001_2 = 73_{10}$
 $not -183_{10}... WRONG!$
 -183_{10} is not in valid range
for 8-bit signed

Representation of Signed Integers

Multiple possible ways:

- Sign magnitude
- Ones' Complement
- Two's Complement (what has been presented)

Two's Complement greatly simplifies addition & subtraction in hardware

- We'll see why when we cover operations
- Generally the only method still used

Representation of Signed Integers

Why the name Two's Complement?

- For a *w*-bit signed representation, we represent -x as $2^w x$
- E.g.: consider the 8-bit representation of -37₁₀

$$2^8 = 256_{10}$$

 $2^8 - 37_{10} = 219_{10}$
 $219_{10} = 11011011_2$ (unsigned)
 $-37_{10} = 11011011_2$ (signed)

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Representing Strings

Strings in C

- Represented by array of characters
- Each character encoded in ASCII format
 - Standard 7-bit encoding of character set
 - Character "0" has code 0x30
- String should be null-terminated
 - Final character = 0
- ASCII characters organized such that:
 - Numeric characters sequentially increase from 0x30
 - Digit *i* has code 0x30+*i*
 - Alphabetic characters sequentially increase in order
 - Uppercase chars 'A' to 'Z' are 0x41 to 0x5A
 - Lowercase chars 'A' to 'Z' are 0x61 to 0x7A
 - Control characters, like <RET>, <TAB>, <BKSPC>, are 0x00 to 0x1A

char	S[6]	=	"18243";
------	------	---	----------

	_
0x31	'1'
0x38	'8'
0x32	'2'
0x34	'4'
0x33	'3'
0x00	null
	term

Intel / Linux

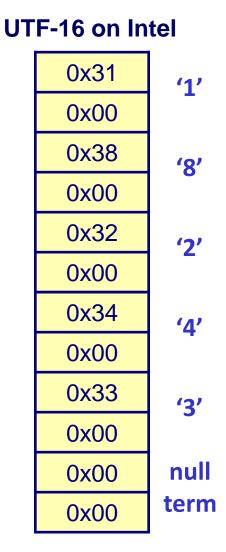
Representing Strings

Limitations of ASCII

- 7-bit encoding limits set of characters to 2⁷ = 128
- 8-bit extended ASCII exists, but still only 2⁸ = 256 chars
- Unable to represent most other languages in ASCII

Answer: Unicode

- first 128 characters are ASCII
 - i.e. 2-byte Unicode for '4': 0x34 -> 0x0034
 - i.e. 4-byte Unicode for 'T': 0x54 -> 0x0000054
- UTF-8: 1-byte version // commonly used
- UTF-16: 2-byte version // commonly used
 - allows 2¹⁶ = 65,536 unique chars
- UTF-32: 4-byte version
 - allows 2³² = ~4 billion unique characters
- Unicode used in many more recent languages, like Java and Python



String Representation Links

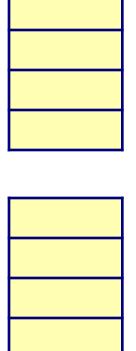
ASCII

- http://www.ascii-code.com/
- Unicode
 - http://unicode-table.com/en/

Quick Check:

Convert the following strings to ASCII-





char name[6] = "Frank";

Data Representation in Memory

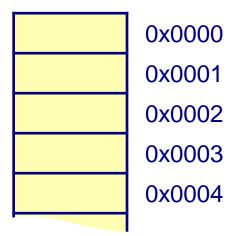
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What is a Pointer?

Recall:

Memory is a contiguous array of individual bytes

Consider a machine with 16-bit addresses



What is a Pointer?

Recall:

Memory is a contiguous array of individual bytes

Consider a machine with 16-bit addresses and 32-bit data



0xA244

0xA245

0xA246

0xA247

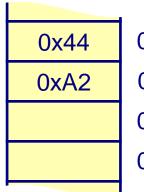
Pointer Representation

Points to a location in memory
 Suppose:

unsigned X = 15398; //0x00003C26

unsigned *ptr = &X; //0xA244

- A pointer is a variable that holds the address of another variable
- Different compilers and machines assign different locations to objects



26

3C

00

00

Endianness

Recall that memory is byte-addressable

• Four bytes in a 32-bit integer, which order are they stored with?

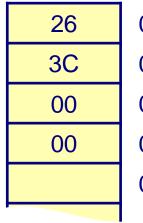
Two ways to store: **unsigned X = 15398; //0x00003C26**

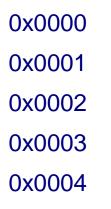
Little Endian

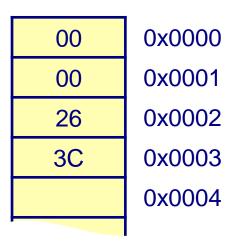
 Least significant bits stored first in memory



 Most significant bits stored first in memory







Quick Check

- Consider the string: char S[6] = "HELLO";
- What is S[0] ?
- What is &S[0] ?
- What is S[3]?
- What is &S[3]?

0x48	'H'	0xACED		
0x45	'E'	0xACEE		
0x4C	Ľ	0xACEF		
0x4C	Ľ	0xACF0		
0x4F	'O'	0xACF1		
0x00	null	0xACF2		
term				